

United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/760,592	01/16/2001	Hisashi Kawabata	P/2635-64	4297
7590 08/18/2005			EXAMINER	
Steven I Weisburd Esq			CHANG, EDITH M	
Dickstein Shap	iro Morin & Oshinsky L	LP		
1177 Avenue of the Americas 41st Floor			ART UNIT	PAPER NUMBER
New York, NY	Y 10036-2714		2637	
				_

DATE MAILED: 08/18/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

				D.			
		Application No.	Applicant(s)				
		09/760,592	KAWABATA, HISASHI				
	Office Action Summary	Examiner	Art Unit				
		Edith M. Chang	2637				
Period f	The MAILING DATE of this commun or Reply	ication appears on the cover sheet	with the correspondence address	-			
A SH THE - Exte after - If th - If NO - Faile Any	IORTENED STATUTORY PERIOD FOR MAILING DATE OF THIS COMMUNI ensions of time may be available under the provisions or SIX (6) MONTHS from the mailing date of this comme period for reply specified above is less than thirty (3) of period for reply is specified above, the maximum stature to reply within the set or extended period for reply reply received by the Office later than three months a need patent term adjustment. See 37 CFR 1,704(b).	CATION. of 37 CFR 1.136(a). In no event, however, may nunication. 0) days, a reply within the statutory minimum of the atutory period will apply and will expire SIX (6) Migwill, by statute, cause the application to become	a reply be timely filed nirty (30) days will be considered timely. DNTHS from the mailing date of this communica ABANDONED (35 U.S.C. § 133).	ation.			
Status		•					
1) 🏹	Responsive to communication(s) file	ed on <i>04 August 2005.</i>					
2a)□	·	2b)⊠ This action is non-final.					
3)	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.						
Disposit	ion of Claims						
5)⊠ 6)⊠ 7)⊠	Claim(s) <u>1-22</u> is/are pending in the at 4a) Of the above claim(s) is/arc Claim(s) <u>1-4,6-8 and 13-16</u> is/are all Claim(s) <u>5,10,17-20 and 22</u> is/are re Claim(s) <u>9,11,12 and 21</u> is/are object Claim(s) are subject to restrict	re withdrawn from consideration. owed. jected. eted to.					
Applicat	ion Papers						
10)⊠	The specification is objected to by the The drawing(s) filed on 16 January 2 Applicant may not request that any object Replacement drawing sheet(s) including The oath or declaration is objected to	001 is/are: a) \square accepted or b) \square ction to the drawing(s) be held in abey the correction is required if the drawing	ance. See 37 CFR 1.85(a). ng(s) is objected to. See 37 CFR 1.12				
Priority	under 35 U.S.C. § 119						
a)	2. Certified copies of the priority3. Copies of the certified copies	documents have been received. documents have been received in of the priority documents have bee nal Bureau (PCT Rule 17.2(a)).	Application No en received in this National Stage				
Attachme	nt(s)						
	ce of References Cited (PTO-892)		v Summary (PTO-413) o(s)/Mail Date				
3) Info	ce of Draftsperson's Patent Drawing Review (P mation Disclosure Statement(s) (PTO-1449 or er No(s)/Mail Date		f Informal Patent Application (PTO-152)				

Art Unit: 2637

DETAILED ACTION

1. The previous final rejection is withdrawn; the new 35 U.S.C. 112 rejections are listed in the following.

Specification

2. The disclosure is objected to because of the following informalities:

On page 24, lines 3-4, "The first despreading circuit 5c" should be "The third dispreading circuit 5c".

Appropriate correction is required.

Claim Objections

3. Claims 5, 9, 11-12, 17 and 21-22 are objected to because of the following informalities:

Claim 5, line 9: "said a third" should be "said third".

Claim 9, line 10: "target phases;" should be "target phases; and".

Claim 11, line 5: "a quasi-coherent" should be "an added quasi-coherent" and line 8: "said quasi-coherent" should be "said added quasi-coherent" to comply with the disclosure of Fig.2 and the description in the specification that a ranking circuit (76 Fig.2) storing raked phases based on the added quasi-coherent signal.

line 10: "plurality of" should be "at least one".

Art Unit: 2637

Claim 12, line 3: "said quasi-coherent" should be "a quasi-coherent"; line 4: "an added" should be "said added"; line 6: "plurality of" should be "at least one".

Claim 17, lines 6 & 10: "said a" should e "said".

Claim 21, line 4: "and a" should be "and an added"; line 7: "ranked phases said phases ranked based" should be "ranked phases based"; line 8: "said quasi-coherent" should be "said added quasi-coherent"; line 9: "setting phase" should be "setting a phase", "plurality of" should be "at least one".

Claim 22, line 3: "said quasi-coherent" should be "a quasi-coherent"; line 4: "an added" should be "said added"; line 7: "a plurality of should be "said at least one".

Appropriate correction is required.

Claim Rejections - 35 USC § 112

4. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

5. Claims 5, 10, 17, 20 and 22 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention.

Art Unit: 2637

Claim 5, lines 5-6: "a dispreading circuit (5a Fig.2) dispreading said first quasicoherent signal (SS1 Fig.2) to produce a despread signal using a third spreading code
sequence having said dispreading phase" does not comply to the disclosure of the
drawings and the description in the specification, wherein "a third spreading code
sequence" is generated from a third spreading code generator (2c Fig.2, page 23
specification) or other code generator having said dispreading phase based on the first
correlation generated by the first correlator (2a Fig.2).

Since claim 5 inheres limitations from claim 1, wherein the synchronization establishing and tracking circuit disclosed in Fig.2, comprises a first spreading code generator (3a) generating a first spreading code sequence and a second spreading code generator (3b) generating a second spreading code sequence, and a phase determining circuit (7 Fig.2) determining a first phase of said first spreading code sequence.

However in claim 5, the synchronization establishing and tracking circuit further comprises a maximum correlation phase determining circuit (4a Fig.2), a despreading circuit (5a Fig.2) despreading the first quasi-coherent signal (SS1) using a third spreading code sequence having the despreading phase determined by the maximum correlation phase determining circuit. The "using a third spreading code sequence having the dispreading phase" of the first quasi-coherent signal (SS1) does not clearly specify the relation to the claim 1.

line 13: "a despreading spreading code sequence" does not clearly indicate that it is "the third spreading code sequence" used to despread the first quasi-coherent signal

Art Unit: 2637

(recited in line 6 of this claim) or another type of spreading code sequence (described in the specification) to despread the first quasi-coherent signal.

Claims 10, 17 & 20 are rejected as the same reason of claim 5. The first despread circuit despreading the first quasi-coherent signal using a third spreading code sequence having the first despreading phase and/or the second despread circuit despreading the second quasi-coherent signal using a fourth spreading code sequence having the second despreading phase do not comply to the subject matter disclosed in the drawing (Fig.2) and described in the specification (pages 20-27).

Claim 22 depends on the rejected claim 20.

6. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 18-19 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claim 18, line 4: "an added semi-synchronous signal" is not clear which or what signal in the disclosure of the drawings or the description of the specification.

Claim 19, line 6: "ranked phases" does not clearly indicate that they are the target phases determined base on added quasi-coherent signal recited in line 3 or not. The setting action recited in line 5 does not cooperate to the action of determining recited in line 3 of this claim.

Application/Control Number: 09/760,592 Page 6

Art Unit: 2637

Allowable Subject Matter

7. Claims 1-4, 6-8 and 13-16 are allowed.

8. Claims 9, 11-12 and 21 would be allowable if rewritten or amended to overcome the objection(s) set forth in this Office action.

- 9. Claims 18 and 19 would be allowable if rewritten or amended to overcome the rejection(s) under 35 U.S.C. 112, 2nd paragraph, set forth in this Office action.
- 10. The following is a statement of reasons for the indication of allowable subject matter:

Claims are allowable over prior art of record because the prior art of record does not teach or suggest, alone or in a combination, among other things, at least a synchronization establishing and tracking circuit and its method ms a whole, the combination of elements and features as claimed, which includes a phase determining circuit determining a first phase of the first spreading code sequence based on an added quasi-coherent signal which is the first and second quasi-coherent signals added together. The first spreading code sequence is correlated with the first quasi-coherent signal, and the second spreading code sequence is correlated with the second quasi-coherent signal as cited in the claims.

11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Edith M. Chang whose telephone number is 571-272-3041. The examiner can normally be reached on M-F.

Art Unit: 2637

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Jay K. Patel can be reached on 571-272-2988. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Edith Chang August 16, 2005

YOUNG T. TSE